

Pin 3: Output

ABSOLUTE MAXIMUM RATINGS

Operating remperature	
S	20 to +85°C
L	40 to +150°C
Supply Voltage, Vcc	16 V
Reverse Supply Voltage, V _{RCC}	16 V
Output Voltage	16 V
Reverse Output Voltage, V _{ROUT}	0.1 V
Output Current Source	3 mA
Output Current Sink	10 mA
Maximum Storage Temperature	65 to 170°C
Maximum Junction Temperature	165°C

Always order by complete part number:

ACS750SCA-050 ACS750LCA-050





TÜV America Certificate Number: U8V 04 11 54214 002 The Allegro ACS75x family of current sensors provides economical and precise solutions for current sensing in industrial, automotive, commercial, and communications systems. The device package allows for easy implementation by the customer. Typical applications include motor control, load detection and management, power supplies, and overcurrent fault protection.

The device consists of a precision, low-offset linear Hall sensor circuit with a copper conduction path located near the die. Applied current flowing through this copper conduction path generates a magnetic field which is sensed by the integrated Hall IC and converted into a proportional voltage. Device accuracy is optimized through the close proximity of the magnetic signal to the Hall transducer. A precise, proportional voltage is provided by the low-offset, chopper-stabilized BiCMOS Hall IC, which is programmed for accuracy at the factory.

The output of the device has a positive slope ($>V_{CC}/2$) when an increasing current flows through the primary copper conduction path (from terminal 4 to terminal 5), which is the path used for current sensing. The internal resistance of this conductive path is typically 130 $\mu\Omega$, providing low power loss. The thickness of the copper conductor allows survival of the device at up to $5\times$ overcurrent conditions. The terminals of the conductive path are electrically isolated from the sensor leads (pins 1 through 3). This allows the ACS75x family of sensors to be used in applications requiring electrical isolation without the use of opto-isolators or other costly isolation techniques.

The device is fully calibrated prior to shipment from the factory. The ACS75x family is lead-free. All leads are coated with 100% matte tin, and there is no lead inside the package. The heavy gauge leadframe is made of oxygen-free copper.

Features and Benefits

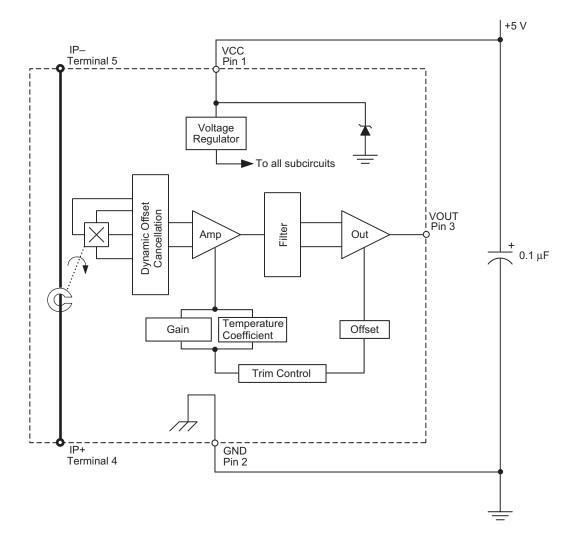
- · Monolithic Hall IC for high reliability
- · Single +5 V supply
- 3 kV_{RMS} isolation voltage between terminals 4/5 and pins 1/2/3
- · Lead-free
- · Automotive temperature range available
- End-of-line factory-trimmed for gain and offset
- Ultra-low power loss: 130 μΩ internal conductor resistance
- Ratiometric output from supply voltage
- · Extremely stable output offset voltage
- Small package size, with easy mounting capability
- · Output proportional to ac and dc currents

Applications

- Industrial systems
- Motor control
- · Power conversion
- · Battery monitors
- · Automotive systems



Functional Block Diagram



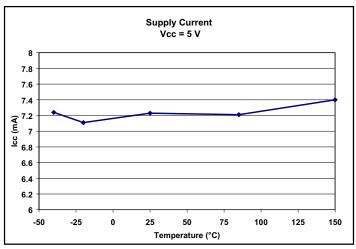


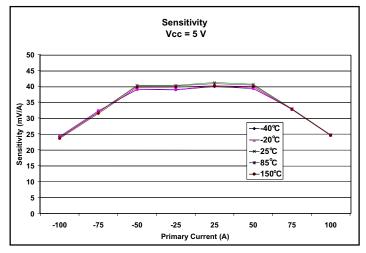
ELECTRICAL CHARACTERISTICS, over temperature unless otherwise stated

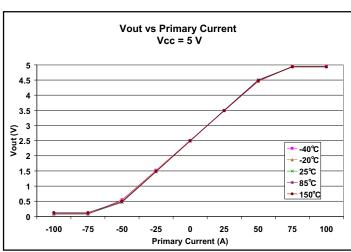
Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Primary Sensed Current	I _P		-50	_	50	Α
Supply Voltage	V _{CC}		4.5	5.0	5.5	V
Supply Current	I _{CC}	V _{CC} = 5.0 V, output open	_	7	10	mA
Output Resistance	R _{OUT}	I _{OUT} = 1.2 mA	_	1	2	Ω
Output Capacitance Load	C _{LOAD}	VOUT to GND		_	10	nF
Output Resistive Load	R _{LOAD}	VOUT to GND	4.7	_	_	kΩ
Primary Conductor Resistance	R _{PRIMARY}	I _P = ±100A, T _A = 25°C	_	130	_	μΩ
Isolation Voltage	V _{ISO}	Pins 1-3 and 4-5, 60 Hz, 1 minute	3.0	_	_	kV
PERFORMANCE CHARACTERI	STICS, -20°C to	+85°C, V _{CC} = 5 V unless otherwise sp	ecified			
Propagation time	t _{PROP}	I _P = ±50 A, T _A = +25°C	_	4	_	μs
Response time	t _{RESPONSE}	I _P = ±50 A, T _A = +25°C	_	27	_	μs
Rise time	t _r	$I_P = \pm 50 \text{ A}, T_A = +25^{\circ}\text{C}$	_	26	_	μs
Frequency Bandwidth	f	-3 dB, T _A = 25°C	_	13	_	kHz
		Over full range of I _P , T _A = 25°C	39	40	42	mV/A
Sensitivity	Sens	Over full range of I _P	36	_	44	mV/A
Noise	V _{NOISE}	Peak-to-neak T. = 25°C		14	_	mV
Nonlinearity	E _{LIN}	Over full range of I _P	_	_	±5	%
Symmetry	E _{SYM}	Over full range of I _P	99	102	105	%
Zero Current Output Voltage	V _{OUT(Q)}	I = 0 A, T _A = 25°C	_	V _{CC} /2	_	V
Electrical Offset Voltage	Electrical Offset Voltage		-60	_	60	mV
(Magnetic error not included)	I = 0 A, T _A = 25°C I = 0 A	-75	_	75	mV	
Magnetic Offset Error	I _{ERROM}	I = 0 A, after excursion of 100 A	_	±0.3	±0.8	Α
Total Output Error		Over full range of I _P , T _A = 25°C	_	±2	-	%
(Including all offsets)	E _{TOT}	Over full range of I _P	_	_	±13	%
PERFORMANCE CHARACTERI	STICS, -40°C to	+150°C, V _{CC} = 5 V unless otherwise s	pecified			
Propagation time	t _{PROP}	I _P = ±50 A	_	4	_	μs
Response time	t _{RESPONSE}	I _P = ±50 A	_	27	_	μs
Rise time	t _r	I _P = ±50 A	_	26	_	μs
Frequency Bandwidth	f	–3 dB, T _A = 25°C	_	13	_	kHz
Sensitivity	Cono	Over full range of I _P , T _A = 25°C	39	40	42	mV/A
	Sens	Over full range of I _P , T _A = 25°C	33	_	46	mV/A
Noise	V _{NOISE}	Peak-to-peak; T _A = +25°C External filter BW = 40 kHz	_	14	-	mV
Nonlinearity	E _{LIN}	Over full range of I _P	_	_	±5	%
Symmetry	E _{SYM}	Over full range of I _P	99	102	105	%
Zero Current Output Voltage	V _{OUT(Q)}	I = 0 A, T _A = 25°C	_	V _{CC} /2	_	V
Jectrical Offset Voltage		I = 0 A, T _A = 25°C	-60	_	60	mV
(Magnetic error not included)	V _{OE}	I = 0 A	-90	_	90	mV
Magnetic Offset Error	I _{ERROM}	I = 0 A, after excursion of 100 A	_	0.3	±0.8	Α
Total Output Error	Total Output Error		_	±2	_	%
(Including all offsets)	E _{TOT}	Over full range of I _P	_	_	±15	%

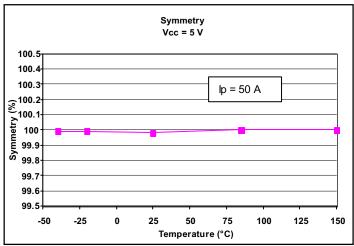


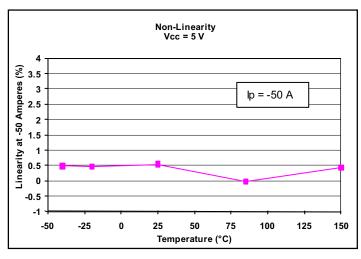
Typical Performance Characteristics

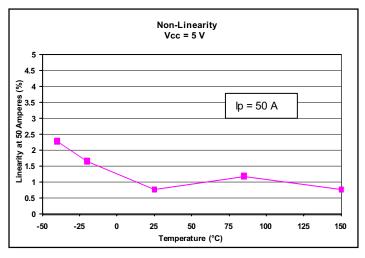




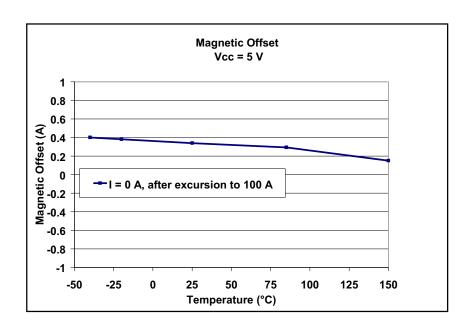


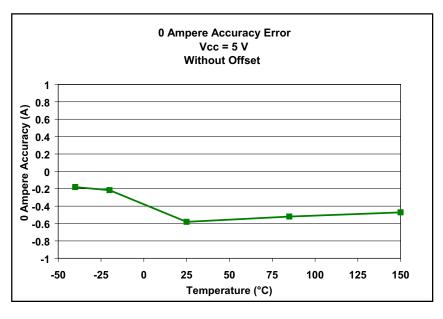






Typical Performance Characteristics







Definitions of Accuracy Characteristics

Sensitivity (Sens): The change in sensor output in response to a 1A change through the primary conductor. The sensitivity is the product of the magnetic circuit sensitivity (G/A) and the linear IC amplifier gain (mV/G). The linear IC amplifier gain is trimmed at the factory to optimize the sensitivity (mV/A) for the full-scale current of the device.

Noise (V_{NOISE}): The product of the linear IC amplifier gain (mV/G) and the noise floor for the Allegro Hall effect linear IC (\approx 1 G). The noise floor is derived from the thermal and shot noise observed in Hall elements. Dividing the noise (mV) by the sensitivity (mV/A) provides the smallest current that the device is able to resolve.

Linearity (E_{LIN}): The degree to which the voltage output from the sensor varies in direct proportion to the primary current through its full-scale amplitude. Linearity reveals the maximum deviation from the ideal transfer curve for this transducer. Nonlinearity in the output can be attributed to the gain variation across temperature and saturation of the flux concentrator approaching the full-scale current. The following equation is used to derive the linearity:

$$100 \left\{ 1 - \left[\frac{\Delta \text{ gain} \times \% \text{ sat } (V_{\text{Out_full-scale amperes}} - V_{\text{OUT(Q)}})}{2 (V_{\text{out_half-scale amperes}} - V_{\text{OUT(Q)}})} \right] \right\}$$

where

 Δ gain = the gain variation as a function of temperature changes from 25°C,

% sat = the percentage of saturation of the flux concentrator, which becomes significant as the current being sensed approaches full-scale $\pm I_P$, and

 $V_{out\ full\text{-scale amperes}}$ = the output voltage (V) when the sensed current approximates full-scale $\pm I_P$.

Symmetry (E_{SYM}): The degree to which the absolute voltage output from the sensor varies in proportion to either a positive or negative full-scale primary current. The following equation is used to derive symmetry:

$$100 \left[\frac{V_{\rm OUT_+full\text{-}scale\ amperes} - V_{\rm OUT_(Q)}}{V_{\rm OUT_(Q)} - V_{\rm out_-full\text{-}scale\ amperes}} \right]$$

Quiescent output voltage ($V_{OUT(Q)}$): The output of the sensor when the primary current is zero. For a unipolar supply voltage, it nominally remains at V_{CC} /2. Thus, V_{CC} = 5 V translates into $V_{OUT(Q)}$ = 2.5 V. Variation in $V_{OUT(Q)}$ can be attributed to the resolution of the Allegro linear IC quiescent voltage trim, magnetic hysteresis, and thermal drift.

Electrical offset voltage (V_{OE}): The deviation of the device output from its ideal quiescent value of $V_{CC}/2$ due to nonmagnetic causes.

Magnetic offset error (I_{ERROM}): The magnetic offset is due to the residual magnetism (remnant field) of the core material. The magnetic offset error is highest when the magnetic circuit has been saturated, usually when the device has been subjected to a full-scale or high-current overload condition. The magnetic offset is largely dependent on the material used as a flux concentrator. The larger magnetic offsets are observed at the lower operating temperatures.

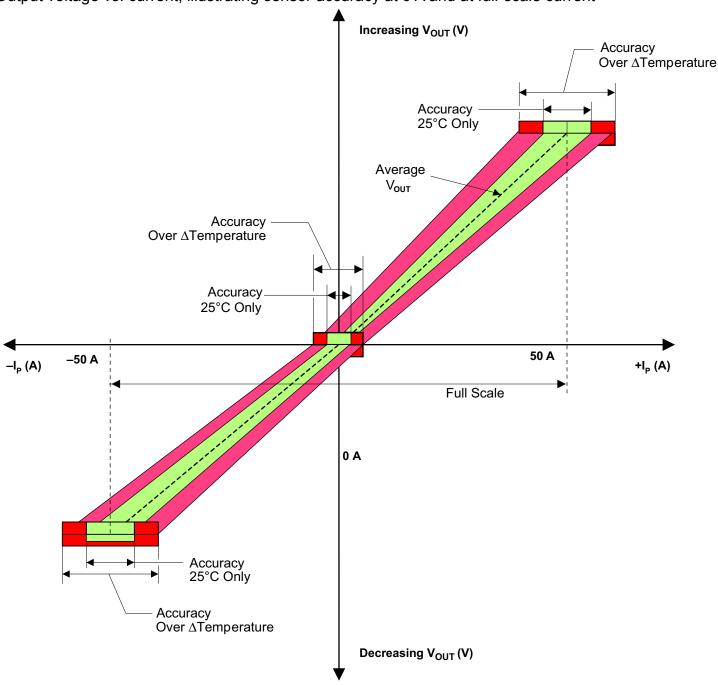
Accuracy (E_{TOT}): The accuracy represents the maximum deviation of the actual output from its ideal value. This is also known as the total output error. The accuracy is illustrated graphically in the Output Voltage versus Current chart on the following page.

Accuracy is divided into four areas:

- 0 A at 25°C: Accuracy of sensing zero current flow at 25°C, without the effects of temperature.
- 0 A over temperature: Accuracy of sensing zero current flow including temperature effects.
- Full-scale current at 25°C: Accuracy of sensing the full-scale current at 25°C, without the effects of temperature.
- Full-scale current over Δ temperature: Accuracy of sensing full-scale current flow including temperature effects.



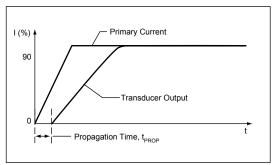
Output voltage vs. current, illustrating sensor accuracy at 0 A and at full-scale current



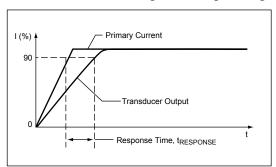


Definitions of Dynamic Response Characteristics

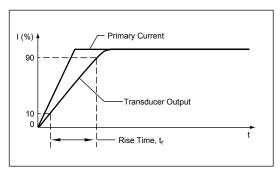
Propagation delay (t_{PROP}): The time required for the sensor output to reflect a change in the primary current signal. Propagation delay is attributed to inductive loading within the linear IC package, as well as in the inductive loop formed by the primary conductor geometry. Propagation delay can be considered as a fixed time offset and may be compensated.



Response time (t_{RESPONSE}): The time interval between a) when the primary current signal reaches 90% of its final value, and b) when the sensor reaches 90% of its output corresponding to the applied current.



Rise time (t_r): The time interval between a) when the sensor reaches 10% of its full scale value, and b) when it reaches 90% of its full scale value. The rise time to a step response is used to derive the bandwidth of the current sensor, in which $f(-3 \text{ dB}) = 0.35/t_r$. Both t_r and $t_{RESPONSE}$ are detrimentally affected by eddy current losses observed in the conductive IC ground plane and, to varying degrees, in the ferrous flux concentrator within the current sensor package.





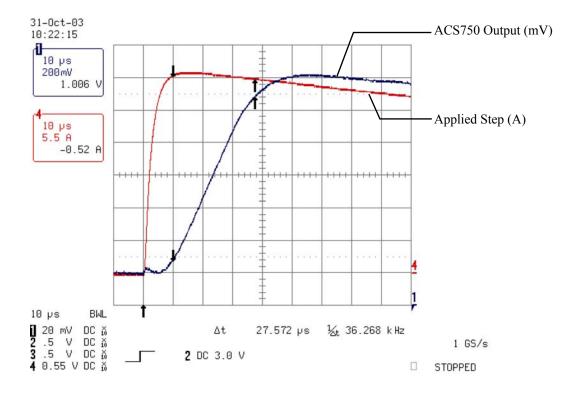
Standards and Physical Specifications

Parameter	Specification
Flammability (package molding compound)	UL recognized to UL 94V-0
Safety	UL recognized to EN 50178
Fire and Electric Shock	UL60950-1:2003 EN60950-1:2001 CAN/CSA C22.2 No. 60950-1:2003
Creepage distance, current terminals to sensor pins	7.25 mm
Clearance distance, current terminals to sensor pins	7.25 mm
Package mass	4.18 g typical

Peak to Peak Noise, applying low-pass filter to ACS750 output

Low Pass Filter Break Frequency	Typical Peak to Peak Noise	
Unfiltered	22.7 mV	
1.4 MHz	21 mV	
24 kHz	7.1 mV	

Step Response, $I_{PRIMARY} = 0$ to 30 A



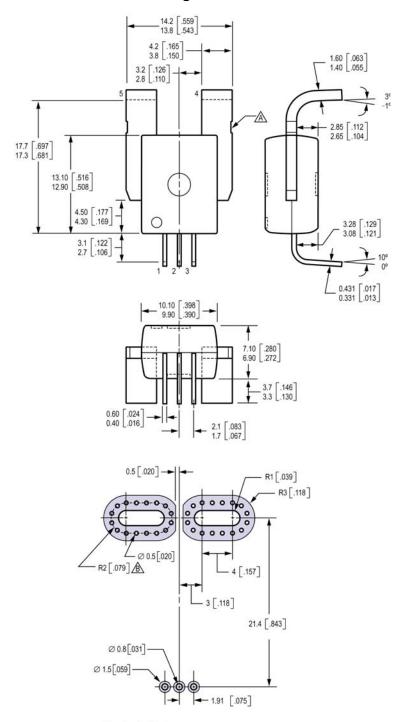


Device Branding Key (Two alternative styles are used)

ACS750 TCA050 YYWWA	ACS	Allegro Current Sensor		
	750	Device family number		
	T	Operating ambient temperature range code [S or L]		
	CA	Package type designator		
	050	Maximum measurable current		
	YY	Manufacturing date code: Calendar year (last two digits)		
	WW	Manufacturing date code: Calendar week		
	A	Manufacturing date code: Shift code		
ACS750 TCA050 LL YYWW	ACS	Allegro Current Sensor		
	750	Device family number		
	T	Operating ambient temperature range code [S or L]		
	CA	Package type designator		
	050	Maximum measurable current		
	LL	Manufacturing lot code		
	YY	Manufacturing date code: Calendar year (last two digits)		
	WW	Manufacturing date code: Calendar week		



Package CA





A Dambar removal intrusion

Perimeter through-holes recommended



The products described herein are manufactured under one or more of the following U.S. patents: 5,045,920; 5,264,783; 5,442,283; 5,389,889; 5,581,179; 5,517,112; 5,619,137; 5,621,319; 5,650,719; 5,686,894; 5,694,038; 5,729,130; 5,917,320; and other patents pending.

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